



## Gokaraju Rangaraju Institute of Engineering and Technology

### Department of Electronics and Communication Engineering

#### Time-Table for Online Classes AY: 2020-21 (I-Semester)

III Year Section A

wef: 24 August 2020

| DAY/HOUR  | 09:00 -10:00 | 10:00 - 10:10 | 10:10 -11:10 | 11:10 - 11:30 | 11:30 -12:30 (For Theory)<br>11:30 - 13:00*(For Lab) |
|-----------|--------------|---------------|--------------|---------------|------------------------------------------------------|
| Monday    | AWP          | <b>BREAK</b>  | CAO          | <b>BREAK</b>  | ICA                                                  |
| Tuesday   | PE1          |               | VLSI         |               | AWP                                                  |
| Wednesday | LCS          |               | AWP          |               | ICA Lab                                              |
| Thursday  | LCS          |               | ICA          |               | VLSI Lab                                             |
| Friday    | VLSI         |               | CAO          |               | OOPS Lab                                             |
| Saturday  | PE1          |               | VLSI         |               | LCS                                                  |

| Sub. Code | Subjects                               | Faculty Name              |                           |
|-----------|----------------------------------------|---------------------------|---------------------------|
| AWP       | Antennas and Wave Propagation          | Dr.K Padmavati            |                           |
| VLSI      | VLSI Design                            | Dr G.Mamatha              |                           |
| LCS       | Linear Control Systems                 | M.Kiran                   |                           |
| ICA       | Integrated Circuit Applications        | G.V Subba Reddy           |                           |
| CAO       | Computer Architecture and Organisation | Dr.V.Arvind               |                           |
| PE1       | OOPS through Java                      | Dr Kavitha                |                           |
| ICA Lab   | Integrated Circuit Lab                 | <b>A1:G.V Subba Reddy</b> | <b>A2:Y.Mareshwar Rao</b> |
| VLSI Lab  | VLSI Design Lab                        | <b>A1: G.Mamatha</b>      | <b>A2: B.Veera Reddy</b>  |
| OOPS Lab  | OOPS through Java Lab                  | <b>A1: K.N.V.Khasim</b>   | <b>A2: V.Vijay Kumar</b>  |

III Year Section B

---

| DAY/HOUR  | 09:00 -10:00 | 10:00 - 10:10 | 10:10 -11:10 | 11:10 - 11:30 | 11:30 -12:30 (For Theory)<br>11:30 - 13:00*(For Lab) |
|-----------|--------------|---------------|--------------|---------------|------------------------------------------------------|
| Monday    | VLSI         | BREAK         | VLSI Lab     | BREAK         | PE 1                                                 |
| Tuesday   | AWP          |               | LCS          |               | PE1                                                  |
| Wednesday | AWP          |               | VLSI         |               | LCS                                                  |
| Thursday  | VLSI         |               | AWP          |               | CAO                                                  |
| Friday    | ICA          |               | LCS          |               | ICA Lab                                              |
| Saturday  | ICA          |               | CAO          |               | OOPS Lab                                             |

| Sub. Code | Subjects                               | Faculty Name          |                    |
|-----------|----------------------------------------|-----------------------|--------------------|
| AWP       | Antennas and Wave Propagation          | Dr.K Padmavati        |                    |
| VLSI      | VLSI Design                            | Dr G.Mamatha          |                    |
| LCS       | Linear Control Systems                 | M.Kiran               |                    |
| ICA       | Integrated Circuit Applications        | G.V Subba Reddy       |                    |
| CAO       | Computer Architecture and Organisation | Dr.V.Arvind           |                    |
| PE1       | OOPS through Java                      | Dr Karuna             |                    |
| ICA Lab   | Integrated Circuit Lab                 | A1:Dr V.Srihari       | A2:Y.Mareshwar Rao |
| VLSI Lab  | VLSI Design Lab                        | A1: M.O.V.Pavan Kumar | A2: B.Veera Reddy  |
| OOPS Lab  | OOPS through Java Lab                  | A1: K.N.V.Khasim      | A2: V.Vijay Kumar  |

### III Year Section C

| DAY/HOUR  | 09:00 -10:00 | 10:00 - 10:10 | 10:10 -11:10 | 11:10 - 11:30 | 11:30 -12:30 (For Theory)<br>11:30 - 13:00*(For Lab) |
|-----------|--------------|---------------|--------------|---------------|------------------------------------------------------|
| Monday    | AWP          | BREAK         | VLSI         | BREAK         | PE1                                                  |
| Tuesday   | ICA          |               | CAO          |               | LCS                                                  |
| Wednesday | AWP          |               | VLSI         |               | OOPS Lab                                             |
| Thursday  | CAO          |               | VLSI         |               | PE1                                                  |
| Friday    | ICA Lab      |               | LCS          |               | AWP                                                  |
| Saturday  | LCS          |               | ICA          |               | VLSI Lab                                             |

| Sub. Code | Subjects                               | Faculty Name             |                           |
|-----------|----------------------------------------|--------------------------|---------------------------|
| AWP       | Antennas and Wave Propagation          | Dr.T.Pavani              |                           |
| VLSI      | VLSI Design                            | G.L.Sumalatha            |                           |
| LCS       | Linear Control Systems                 | M.Suneetha               |                           |
| ICA       | Integrated Circuit Applications        | G.Shruthi                |                           |
| CAO       | Computer Architecture and Organisation | Dr.V.Arvind              |                           |
| PE1       | OOPS through Java                      | Dr Kavitha               |                           |
| ICA Lab   | Integrated Circuit Lab                 | <b>A1:M.Suneetha</b>     | <b>A2:Y.Mareshwar Rao</b> |
| VLSI Lab  | VLSI Design Lab                        | <b>A1: G.L.Sumalatha</b> | <b>A2: B.Veera Reddy</b>  |
| OOPS Lab  | OOPS through Java Lab                  | <b>A1: B.Shilpa</b>      | <b>A2: G.Shruthi</b>      |

### III Year Section D

| DAY/HOUR  | 09:00 -10:00 | 10:00 - 10:10 | 10:10 -11:10 | 11:10 - 11:30 | 11:30 -12:30 (For Theory)<br>11:30 - 13:00*(For Lab) |
|-----------|--------------|---------------|--------------|---------------|------------------------------------------------------|
| Monday    | PE1          | <b>BREAK</b>  | VLSI         | <b>BREAK</b>  | OOPS Lab                                             |
| Tuesday   | CAO          |               | AWP          |               | ICA                                                  |
| Wednesday | ICA          |               | LCS          |               | AWP                                                  |
| Thursday  | LCS          |               | CAO          |               | ICA Lab                                              |
| Friday    | AWP          |               | VLSI Lab     |               | VLSI                                                 |
| Saturday  | PE1          |               | VLSI         |               | LCS                                                  |

| Sub. Code | Subjects                               | Faculty Name         |                           |
|-----------|----------------------------------------|----------------------|---------------------------|
| AWP       | Antennas and Wave Propagation          | Dr.T.Pavani          |                           |
| VLSI      | VLSI Design                            | Dr.D.Jayanthi        |                           |
| LCS       | Linear Control Systems                 | M.Suneetha           |                           |
| ICA       | Integrated Circuit Applications        | G.Shruthi            |                           |
| CAO       | Computer Architecture and Organisation | Dr.K.Meenakshi       |                           |
| PE1       | OOPS through Java                      | Swathi               |                           |
| ICA Lab   | Integrated Circuit Lab                 | <b>A1:M.Suneetha</b> | <b>A2:Y.Mareshwar Rao</b> |

|          |                       |                          |                              |
|----------|-----------------------|--------------------------|------------------------------|
| VLSI Lab | VLSI Design Lab       | <b>A1:</b> G.L.Sumalatha | <b>A2:</b> M.O.V.Pavan kumar |
| OOPS Lab | OOPS through Java Lab | <b>A1:</b> B.Shilpa      | <b>A2:</b> G.Shruthi         |

### III Year Section E

| DAY/HOUR  | 09:00 -10:00 | 10:00 - 10:10 | 10:10 -11:10 | 11:10 - 11:30 | 11:30 -12:30 (For Theory)<br>11:30 - 13:00*(For Lab) |
|-----------|--------------|---------------|--------------|---------------|------------------------------------------------------|
| Monday    | CAO          | <b>BREAK</b>  | LCS          | <b>BREAK</b>  | ICA                                                  |
| Tuesday   | VLSI         |               | ICA          |               | VLSI Lab                                             |
| Wednesday | AWP          |               | VLSI         |               | LCS                                                  |
| Thursday  | AWP          |               | VLSI         |               | OOPS Lab                                             |
| Friday    | PE1          |               | AWP          |               | CAO                                                  |
| Saturday  | PE1          |               | ICA Lab      |               | LCS                                                  |

| Sub. Code | Subjects                               | Faculty Name             |                              |
|-----------|----------------------------------------|--------------------------|------------------------------|
| AWP       | Antennas and Wave Propagation          | A.Ushasree               |                              |
| VLSI      | VLSI Design                            | Dr.D.Jayanthi            |                              |
| LCS       | Linear Control Systems                 | K.Swathi                 |                              |
| ICA       | Integrated Circuit Applications        | Dr.V.Srihari             |                              |
| CAO       | Computer Architecture and Organisation | Dr.K.Meenakshi           |                              |
| PE1       | OOPS through Java                      | Dr Anuradha              |                              |
| ICA Lab   | Integrated Circuit Lab                 | <b>A1:Dr.V.Srihari</b>   | <b>A2:Y.Mareshwar Rao</b>    |
| VLSI Lab  | VLSI Design Lab                        | <b>A1:</b> B.Veera Reddy | <b>A2:</b> M.O.V.Pavan Kumar |
| OOPS Lab  | OOPS through Java Lab                  | <b>A1:</b> V.Vijay Kumar | <b>A2:</b> K.Swathi          |